

Docket No. AUS920030468US1

ABSTRACT OF THE DISCLOSURE

METHOD AND APPARATUS FOR COORDINATING DYNAMIC MEMORY DEALLOCATION WITH A REDUNDANT BIT LINE STEERING MECHANISM

A method and apparatus for coordinating dynamic memory page deallocation with a redundant bit line steering mechanism are provided. With the method and apparatus, memory scrubbing and redundant bit line steering operations are performed in parallel with handling of notifications of runtime correctable errors. When a correctable error is encountered during runtime, and the correctable error is determined to be persistent, then dynamic memory page deallocation is requested of a hypervisor. The determination of persistence is based on a history CE table that is populated by the operation of the memory scrubbing and redundant bit line steering mechanism of a service processor. Thus, only those correctable errors that persist for longer than one memory scrubbing cycle are subject to memory page deallocation.